

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2582	dang-d\$.in. or fu-c\$.in. or harms-m\$.in. or altera.as.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:35
L2	15	L1 and (functional adj blocks).clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:35
L3	7757	(326/41 or 326/38 or 714/724 or 714/726 or 714/734 or 714/731 or 714/30).cdls.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L4	137	L3 and (cpld or pld or fpga or isp or (programmable adj logic adj device)) and (configura\$4) and scan.clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L5	97	L4 and test\$3.clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L6	49	L5 and core	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L7	8938	(326/41 326/38 326/40 714/724 714/727 714/725 714/729 714/742 714/726 714/734 714/731 714/30).cdls.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L8	192	L7 and (cpld or pld or fpga or isp or (programmable adj logic adj device)) and (configura\$4) and scan.clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L9	140	L8 and test\$3.clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L10	87	L9 and (scan adj (register chain cell latch)).clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40

L11	30	L10 and (input near "3" (mux multiplexer multiplexor).clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:40
L12	6	((configur\$5 near3 ((unit device circuit) and test\$3)) and (logic with multiplexer with gate).clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/07 07:44
S83	2	"6996758".pn. "6114892".pn.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/06 16:13
S84	1	"7058867".pn.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/06 17:05
S85	0	10/750,175	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/06 17:12
S86	2153	(scan adj (cell register latch flip)) and input and test\$3. clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/06 17:14
S87	169	S86 and (multiplexer and (gate "AND")).clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/06 17:16
S88	138	S87 and (data).clm.	US_PGPUB; USPAT; IBM_TDB	OR	ON	2008/05/06 17:16

5/7/08 7:47:45 AM

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